



Docket No. p7112

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

*Amend*  
*H/C 11-20-02*  
*A. Wall*

In re Patent Application of:  
**Dabral et al.**

Examiner: **Chang, D.**

Application No.: **09/451,684**

Art Unit: **2819**

Filed: **30 November 1999**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231

For: **Input-Output Bus Interface**

on **November 4, 2002**

Date of Deposit

**Carrie Boccaccini**

Name of Person Mailing Correspondence

**11-4-2002**

Assistant Commissioner for Patents  
Washington, D.C. 20231

*Carrie Boccaccini*  
Signature  
at 2002-11-04

Date

**RESPONSE C**

**Request for Reconsideration and Amendment under CFR §1.111**

Assistant Commissioner for Patents:

In response to the Office Action dated 12 August 2002, Applicants respectfully request reconsideration of the above-identified patent application as amended below.

**In the Claims**

Please amend claims 22, 31-33, 38, and 39 to read as shown below.

Please cancel without prejudice claims 35 - 37.

For convenience, all presently active claims are shown below.

RECEIVED  
NOV 18 2002  
TECHNOLOGY CENTER 2800

22. (Amended)

An electronic system comprising:

a ground having a ground voltage;

a transmission line having a first end with a first characteristic impedance, a second end with a second characteristic impedance, and a quiescent voltage at ground voltage;

*Sub 017*  
*C1*